Dear Madam/Sir,

I am a **Ph.D. candidate** working under the guidance of Prof. Rajesh Gupta and Prof. Sorin Lerner at University of California, San Diego (UCSD). My interest lies broadly in the area of program analysis, software engineering, and verification. More specifically, during my doctoral studies I have worked on modeling, high-level synthesis and formal verification techniques for system designs. Furthermore, I have extensive hands on experience in embedded systems, operating systems, compilers, theorem provers and various verification techniques.

For my dissertation I am working on "**High-Level Verification of System Designs**". In this direction, during the last couple of years I have published papers in top tier conferences, like **PLDI '09**, **CAV '08**, **DAC '08**, and **ICCAD '07**. Moreover, I have few more papers in submission and in preparation. Each of the above work describes a complete and practical approach that significantly improves the current state of the art.

Before joining UCSD, I received my 5 year Integrated Master of Science degree in **Mathematics and Computing** from Indian Institute of Technology, Kharagpur in 2004. Throughout my stay at IIT I have tried to maintain a steady grade point average (GPA) and I was **ranked first in the department**.

I am interested in getting a **full-time position** at your company from summer of 2009. I shall consider myself privileged if I get an opportunity to work at your esteemed company and I trust that I will be able to contribute my best.

Attached you will find my CV and research statement for your kind perusal.

I look forward to receiving your reply.

Yours sincerely,

Sudipta Kundu
http://mesl.ucsd.edu/sudipta